

a plurality of logic circuits to output selection signals to the plurality of selecting thin film transistors, respectively;

a shift register connected to a first terminal of each of the plurality of logic circuits; and

a mask signal generation circuit to output a mask signal to a second terminal of the plurality of logic circuits.

34. The active matrix type display device according to claim 33 wherein said display device is an EL display.

35. The active matrix type display device according to claim 33 wherein said display device is an organic EL display.

36. The active matrix type display device according to claim 33 wherein said display device is an inorganic EL display.

37. The active matrix type display device according to claim 33 wherein said display device is a liquid crystal device.

38. The active matrix type display device according to claim 33 further comprising field emission diodes in the picture elements.

39. An active matrix type display device comprising:
a plurality of first signal lines extending in parallel over a substrate;
a plurality of second signal lines extending across the plurality of first signal lines to define a plurality of picture elements arranged in a matrix form over the substrate;
at least one first thin film transistor provided in each of the plurality of picture elements wherein a gate of the first thin film transistor is connected to a corresponding one of the second signal lines;

at least one second thin film transistor provided in each of the plurality of picture elements wherein a gate of the second thin film transistor is electrically connected to one terminal of the first thin film transistor;

EL
cont